

**Scholar Details:**

<b>S. No</b>	<b>Scholar Name, Designation and Institution</b>	<b>Supervisor Name, Designation and Institution</b>	<b>Year of Reg. &amp; Reg. No.</b>	<b>Title of Research work</b>	<b>Status of PhD (Coursework completed/Synopsis submitted/Thesis submitted/Vivo voce completed)</b>
1.	Mr.Silambarasan D Anna University Regional Centre, Coimbatore	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jan 2015 & 1513469430	High speed residue system for cryptography applications	Coursework completed
2.	Maragatharaj S Knowledge Institute of Technology Kakapalayam, Chennai	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jan 2016 & 1613469289	Design of massive input multi output low power variable digital filter banks for ultra wide band spectrum sensing in cognitive radio systems.	Coursework completed
3.	Mr.Nelson Kingsley Joel.P AP/ECE Sri Ranganathan Engineering College, Coimbatore	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	July 2015 & 1514469960	Implementation of Efficient Algorithms for Removal of Noises in Digital Images	Coursework completed
4.	Mr.Lenin Raja R AP/ECE Sri Vidhya College of Engg. & Tech., Virudhunagar	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jan 2015 & 1514469152	An Enhanced switching technique for reducing power consumption using multi dynamic node	Doing Course work
5.	Mr.Nagarajan S Assistant Engineer, Tamilnadu Electricity Board, Devanurpudur, Pollachi	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jan 2015 & 1514469434	Design of Bidirectional Switched Boost Converter for Efficient over Flow in AC/DC Hybrid Microgrid	Coursework completed
6.	Mr.Senthilvel A Assistant Executive Engineer (O&M), Tamilnadu Electricity Board, Pollachi	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jan 2015 & 1514469435	Efficient Design of Solar Power Plant Using Soft Computing Techniques and FPGA	Coursework completed

7.	Ms.P.Gnanambikai, Lecturer(SG)/ECE, NPTC,Pollachi	Dr.K.N.Vijeyakumar Associate Professor, Electronics and Communication Engineering	Jun 2016 & 16244697154	Design of Efficient VLSI architecture for image De- Noising	Coursework completed
----	---	--	---------------------------	---	-------------------------